

Succinct Proofs

GKR

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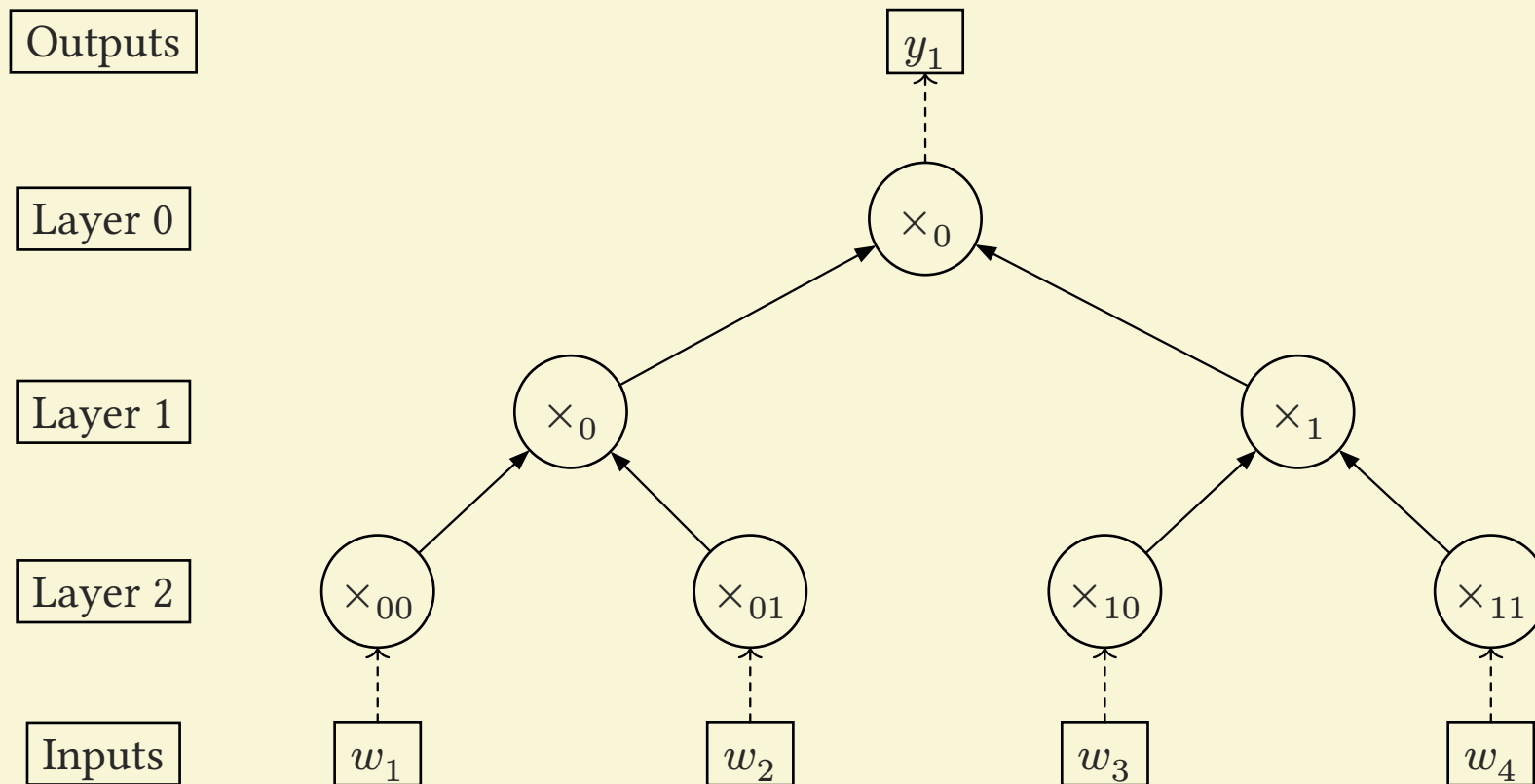
The GKR Protocol

Layered arithmetic circuit $\mathcal{C}(\boldsymbol{w} \in \mathbb{F}^n) = \boldsymbol{y} \in \mathbb{F}^m$

- Only addition and multiplication gates
- n inputs
- m outputs,
- d layers

Example

$$S_0 = 1, S_1 = 2, S_2 = 4, s_i = \lg(S_i), n = 4, m = 1, d = 3$$



$$W_i(\mathbf{a}) \in \mathbb{B}^{s_i} \rightarrow \mathbb{F}$$

$$\text{add}_i(\mathbf{a}, \mathbf{b}, \mathbf{c}) \in \mathbb{B}^{s_i+2s_{i+1}} \rightarrow \mathbb{B}$$

$$\text{mul}_i(\mathbf{a}, \mathbf{b}, \mathbf{c}) \in \mathbb{B}^{s_i+2s_{i+1}} \rightarrow \mathbb{B}$$

Polynomial Extension of W_i

$$W_i(\mathbf{a}) \in \mathbb{B}^{s_i} \rightarrow \mathbb{F}$$

$$\text{add}_i(\mathbf{a}, \mathbf{b}, \mathbf{c}) \in \mathbb{B}^{s_i+2s_{i+1}} \rightarrow \mathbb{B}$$

$$\text{mul}_i(\mathbf{a}, \mathbf{b}, \mathbf{c}) \in \mathbb{B}^{s_i+2s_{i+1}} \rightarrow \mathbb{B}$$

$$\begin{aligned} \tilde{W}_i(\mathbf{a}) = & \sum_{\mathbf{b}, \mathbf{c} \in \mathbb{B}^{s_{i+1}}} \widetilde{\text{add}}_i(\mathbf{a}, \mathbf{b}, \mathbf{c}) \left(\tilde{W}_{i+1}(\mathbf{b}) + \tilde{W}_{i+1}(\mathbf{c}) \right) + \\ & \widetilde{\text{mul}}_i(\mathbf{a}, \mathbf{b}, \mathbf{c}) \cdot \tilde{W}_{i+1}(\mathbf{b}) \cdot \tilde{W}_{i+1}(\mathbf{c}) \end{aligned}$$

Proving the Output of the Circuit

- Prover claims output of circuit is $y' \cong W'$
- Actual output is $y \cong W_0$

Proving the Output of the Circuit

- Prover claims output of circuit is $\mathbf{y}' \cong W'$
- Actual output is $\mathbf{y} \cong W_0$

$$\mathbf{r} \in_R \mathbb{F}^{s_0} : \tilde{W}'(\mathbf{r}) = \tilde{W}_0(\mathbf{r}) \implies \tilde{W}' = \tilde{W}_0 \implies W' = W_0$$

$$\begin{aligned}\widetilde{W}_0(\boldsymbol{r}) = & \sum_{\boldsymbol{b}, \boldsymbol{c} \in \mathbb{B}^{s_1}} \widetilde{\text{add}}_0(\boldsymbol{r}, \boldsymbol{b}, \boldsymbol{c}) \left(\widetilde{W}_1(\boldsymbol{b}) + \widetilde{W}_1(\boldsymbol{c}) \right) + \\ & \widetilde{\text{mul}}_0(\boldsymbol{r}, \boldsymbol{b}, \boldsymbol{c}) \cdot \widetilde{W}_1(\boldsymbol{b}) \cdot \widetilde{W}_1(\boldsymbol{c})\end{aligned}$$

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Sumcheck polynomial

$$f_0(\mathbf{b}, \mathbf{c}) = \widetilde{\text{add}}_0(\mathbf{r}, \mathbf{b}, \mathbf{c}) \left(\tilde{W}_1(\mathbf{b}) + \tilde{W}_1(\mathbf{c}) \right) + \widetilde{\text{mul}}_0(\mathbf{r}, \mathbf{b}, \mathbf{c}) \cdot \tilde{W}_1(\mathbf{b}) \cdot \tilde{W}_1(\mathbf{c})$$

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Last round of sumcheck

$$\mathbf{b}'_1, \mathbf{c}'_1 \in_R \mathbb{F}^{s_{i+1}}$$

$$\begin{aligned}f_0(\mathbf{b}'_1, \mathbf{c}'_1) = \widetilde{\text{add}}_0(\mathbf{r}, \mathbf{b}'_1, \mathbf{c}'_1) \big(\tilde{W}_1(\mathbf{b}'_1) + \tilde{W}_1(\mathbf{c}'_1) \big) + \\ \widetilde{\text{mul}}_0(\mathbf{r}'_0, \mathbf{b}'_1, \mathbf{c}'_1) \cdot \tilde{W}_1(\mathbf{b}'_1) \cdot \tilde{W}_1(\mathbf{c}'_1)\end{aligned}$$

Idea: Verify Evaluations using Sumcheck

$$f_1(\mathbf{b}, \mathbf{c}) = \widetilde{\text{add}}_1(\mathbf{b}'_0, \mathbf{b}, \mathbf{c}) \left(\widetilde{W}_2(\mathbf{b}) + \widetilde{W}_2(\mathbf{c}) \right) + \widetilde{\text{mul}}_1(\mathbf{b}'_1, \mathbf{b}, \mathbf{c}) \cdot \widetilde{W}_2(\mathbf{b}) \cdot \widetilde{W}_2(\mathbf{c})$$

$$f_1(\mathbf{b}, \mathbf{c}) = \widetilde{\text{add}}_1(\mathbf{c}'_0, \mathbf{b}, \mathbf{c}) \left(\widetilde{W}_2(\mathbf{b}) + \widetilde{W}_2(\mathbf{c}) \right) + \widetilde{\text{mul}}_1(\mathbf{c}'_1, \mathbf{b}, \mathbf{c}) \cdot \widetilde{W}_2(\mathbf{b}) \cdot \widetilde{W}_2(\mathbf{c})$$

Exponential!

Combining two claims to one

$$\begin{aligned} q(\mathbf{b}'_0, \mathbf{c}'_0) &= \widetilde{W}_1(\mathbf{b}'_0) + \alpha \cdot \widetilde{W}_1(\mathbf{c}'_0) \\ &= \left(\sum_{\mathbf{b}, \mathbf{c} \in \mathbb{B}^{s_2}} \widetilde{\text{add}}_1(\mathbf{b}'_0, \mathbf{b}, \mathbf{c}) (\widetilde{W}_2(\mathbf{b}) + \widetilde{W}_2(\mathbf{c})) + \widetilde{\text{mul}}_1(\mathbf{b}'_0, \mathbf{b}, \mathbf{c}) \cdot \widetilde{W}_2(\mathbf{b}) \cdot \widetilde{W}_2(\mathbf{c}) \right) + \\ &\quad \alpha \cdot \left(\sum_{\mathbf{b}, \mathbf{c} \in \mathbb{B}^{s_2}} \widetilde{\text{add}}_1(\mathbf{c}'_0, \mathbf{b}, \mathbf{c}) (\widetilde{W}_2(\mathbf{b}) + \widetilde{W}_2(\mathbf{c})) + \widetilde{\text{mul}}_1(\mathbf{c}'_0, \mathbf{b}, \mathbf{c}) \cdot \widetilde{W}_2(\mathbf{b}) \cdot \widetilde{W}_2(\mathbf{c}) \right) \\ &= \sum_{\mathbf{b}, \mathbf{c} \in \mathbb{B}^{s_2}} \widetilde{\text{add}}_1(\mathbf{b}'_0, \mathbf{b}, \mathbf{c}) (\widetilde{W}_2(\mathbf{b}) + \widetilde{W}_2(\mathbf{c})) + \widetilde{\text{mul}}_1(\mathbf{b}'_0, \mathbf{b}, \mathbf{c}) \cdot \widetilde{W}_2(\mathbf{b}) \cdot \widetilde{W}_2(\mathbf{c}) + \\ &\quad \alpha \cdot \widetilde{\text{add}}_1(\mathbf{c}'_0, \mathbf{b}, \mathbf{c}) (\widetilde{W}_2(\mathbf{b}) + \widetilde{W}_2(\mathbf{c})) + \alpha \cdot \widetilde{\text{mul}}_1(\mathbf{c}'_0, \mathbf{b}, \mathbf{c}) \cdot \widetilde{W}_2(\mathbf{b}) \cdot \widetilde{W}_2(\mathbf{c}) \\ &= \sum_{\mathbf{b}, \mathbf{c} \in \mathbb{B}^{s_2}} \left(\widetilde{\text{add}}_1(\mathbf{b}'_0, \mathbf{b}, \mathbf{c}) + \alpha \cdot \widetilde{\text{add}}_1(\mathbf{c}'_0, \mathbf{b}, \mathbf{c}) \right) (\widetilde{W}_2(\mathbf{b}) + \widetilde{W}_2(\mathbf{c})) + \\ &\quad \left(\widetilde{\text{mul}}_1(\mathbf{b}'_0, \mathbf{b}, \mathbf{c}) + \alpha \cdot \widetilde{\text{mul}}_1(\mathbf{c}'_0, \mathbf{b}, \mathbf{c}) \right) (\widetilde{W}_2(\mathbf{b}) \cdot \widetilde{W}_2(\mathbf{c})) \end{aligned}$$

Combining two claims to one

We have:

$$v_1 := p(\mathbf{r}_1), v_2 := p(\mathbf{r}_2)$$

$$q(X_1, \dots, X_{2\ell}) := p(X_1, \dots, X_\ell) + \alpha \cdot p(X_{\ell+1}, \dots, X_{2\ell}g)$$

$$q(\mathbf{r}_1, \mathbf{r}_2) \stackrel{?}{=} v_1 + \alpha \cdot v_2$$

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$$q(X_1, \dots, X_{2\ell}) := p(X_1, \dots, X_\ell) + \alpha \cdot p(X_{\ell+1}, \dots, X_{2\ell})$$

$$q(\mathbf{r}_1, \mathbf{r}_2) \stackrel{?}{=} v_1 + \alpha \cdot v_2$$

Proof: Assume $v_1 \neq p(\mathbf{r}_1) \vee v_2 \neq p(\mathbf{r}_2)$, but q is defined as above:

$$e(X) = v_1 + X \cdot v_2 - (p(\mathbf{r}_1) + X \cdot p(\mathbf{r}_2))$$

$$\Pr[e(\alpha) = 0 \mid e(X) \neq 0] = \frac{\deg(e)}{|\mathbb{F}|} = \frac{1}{|\mathbb{F}|}$$

Combining two claims to one

The next round we sumcheck:

$$f_1(\mathbf{b}, \mathbf{c}) := \left(\widetilde{\text{add}}_1(\mathbf{b}'_0, \mathbf{b}, \mathbf{c}) + \alpha \cdot \widetilde{\text{add}}_1(\mathbf{c}'_0, \mathbf{b}, \mathbf{c}) \right) \left(\widetilde{W}_2(\mathbf{b}) + \widetilde{W}_2(\mathbf{c}) \right) + \\ \left(\widetilde{\text{mul}}_1(\mathbf{b}'_0, \mathbf{b}, \mathbf{c}) + \alpha \cdot \widetilde{\text{mul}}_1(\mathbf{c}'_0, \mathbf{b}, \mathbf{c}) \right) \left(\widetilde{W}_2(\mathbf{b}) \cdot \widetilde{W}_2(\mathbf{c}) \right)$$

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And the verifier checks:

$$m_i \stackrel{?}{=} \left(\widetilde{\text{add}}_i(\mathbf{b}'_{i-1}, \mathbf{b}'_i, \mathbf{c}'_i) + \alpha \cdot \widetilde{\text{add}}_i(\mathbf{c}'_{i-1}, \mathbf{b}'_i, \mathbf{c}'_i) \right) \left(v_{\mathbf{b}'_i} + v_{\mathbf{c}'_i} \right) + \\ \left(\widetilde{\text{mul}}_i(\mathbf{b}'_{i-1}, \mathbf{b}'_i, \mathbf{c}'_i) + \alpha \cdot \widetilde{\text{mul}}_i(\mathbf{c}'_{i-1}, \mathbf{b}'_i, \mathbf{c}'_i) \right) \left(v_{\mathbf{b}'_i} \cdot v_{\mathbf{c}'_i} \right)$$

Full Protocol: Preprocessing

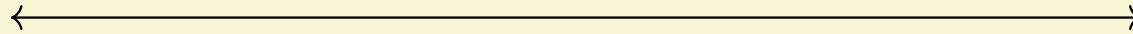
$\mathcal{P}$ sends W' to $\mathcal{U}$ claiming that $W' = W_0$. $\mathcal{U}$ then picks out a random $\mathbf{r}$. After this point, $\mathcal{P}$ wants to prove that $m_0 = \tilde{W}_0(\mathbf{r})$.

$$W' : \mathbb{B}^{s_0} \rightarrow \mathbb{F} \xrightarrow{W'} \mathbf{r} \in_R \mathbb{F}^{s_0}, m_0 := \tilde{W}'(\mathbf{r})$$

Full Protocol: Round zero

$$f_0(\mathbf{b}, \mathbf{c}) = \widetilde{\text{add}}_0(\mathbf{r}, \mathbf{b}, \mathbf{c}) \left(\widetilde{W}_1(\mathbf{b}) + \widetilde{W}_1(\mathbf{c}) \right) + \widetilde{\text{mul}}_0(\mathbf{r}, \mathbf{b}, \mathbf{c}) \cdot \widetilde{W}_1(\mathbf{b}) \cdot \widetilde{W}_1(\mathbf{c})$$

$$\sum_{\mathbf{b}, \mathbf{c} \in \mathbb{B}^{s_1}} f_0(\mathbf{b}, \mathbf{c}) \stackrel{?}{=} m_0$$



At the end of the protocol, $\mathcal{P}$ sends $\mathcal{V}$ the evaluations of $v_{\mathbf{b}'_0} := \widetilde{W}_1(\mathbf{b}'_0)$ and $v_{\mathbf{c}'_0} := \widetilde{W}_1(\mathbf{c}'_0)$, so $\mathcal{V}$ can make the final check in the sumcheck protocol.

$$v_{\mathbf{b}'_0} := \widetilde{W}_1(\mathbf{b}'_0), v_{\mathbf{c}'_0} := \widetilde{W}_1(\mathbf{c}'_0) \xrightarrow{v_{\mathbf{b}'_0}, v_{\mathbf{c}'_0}} m_0 \stackrel{?}{=} \widetilde{\text{add}}_0(\mathbf{r}, \mathbf{b}'_0, \mathbf{c}'_0) \cdot (v_{\mathbf{b}'_0} + v_{\mathbf{c}'_0}) + \widetilde{\text{mul}}_0(\mathbf{r}, \mathbf{b}'_0, \mathbf{c}'_0) \cdot v_{\mathbf{b}'_0} \cdot v_{\mathbf{c}'_0}$$

Full Protocol: Round i

$$f_i(\mathbf{b}, \mathbf{c}) := \left(\widetilde{\text{add}}_i(\mathbf{b}'_{i-1}, \mathbf{b}, \mathbf{c}) + \alpha \cdot \widetilde{\text{add}}_i(\mathbf{c}'_{i-1}, \mathbf{b}, \mathbf{c}) \right) \left(\widetilde{W}_{i+1}(\mathbf{b}) + \widetilde{W}_{i+1}(\mathbf{c}) \right) + \\ \left(\widetilde{\text{mul}}_i(\mathbf{b}'_{i-1}, \mathbf{b}, \mathbf{c}) + \alpha \cdot \widetilde{\text{mul}}_i(\mathbf{c}'_{i-1}, \mathbf{b}, \mathbf{c}) \right) \left(\widetilde{W}_{i+1}(\mathbf{b}) \cdot \widetilde{W}_{i+1}(\mathbf{c}) \right)$$

$$f_i(\mathbf{b}, \mathbf{c}) \xleftarrow{\alpha} \alpha \in_R \mathbb{F}$$

$$\sum_{\mathbf{b}, \mathbf{c} \in \mathbb{B}^{s_{i+1}}} f_i(\mathbf{b}, \mathbf{c}) \stackrel{?}{=} m_i$$

$$\longleftrightarrow$$

$$m'_i := \left(\widetilde{\text{add}}_i(\mathbf{b}'_{i-1}, \mathbf{b}'_i, \mathbf{c}'_i) + \alpha \cdot \widetilde{\text{add}}_i(\mathbf{c}'_{i-1}, \mathbf{b}'_i, \mathbf{c}'_i) \right) (v_{\mathbf{b}'_i} + v_{\mathbf{c}'_i}) + \\ \left(\widetilde{\text{mul}}_i(\mathbf{b}'_{i-1}, \mathbf{b}'_i, \mathbf{c}'_i) + \alpha \cdot \widetilde{\text{mul}}_i(\mathbf{c}'_{i-1}, \mathbf{b}'_i, \mathbf{c}'_i) \right) (v_{\mathbf{b}'_i} \cdot v_{\mathbf{c}'_i})$$

$$v_{\mathbf{b}'_i} := \widetilde{W}_{i+1}(\mathbf{b}'_i), \quad v_{\mathbf{b}'_i}, v_{\mathbf{c}'_i} \xrightarrow{\quad} m_i \stackrel{?}{=} m'_i$$

$$v_{\mathbf{c}'_i} := \widetilde{W}_{i+1}(\mathbf{c}'_i)$$

At the input layer d , $\mathcal{V}$ has two claims $v_{\mathbf{b}'_{d-1}}$ and $v_{\mathbf{c}'_{d-1}}$. $\mathcal{V}$ constructs $\tilde{W}_d$ from w . $\mathcal{V}$ then finally checks that $\tilde{W}_d(\mathbf{b}'_{d-1}) \stackrel{?}{=} v_{\mathbf{b}'_{d-1}}$ and $\tilde{W}_d(\mathbf{c}'_{d-1}) \stackrel{?}{=} v_{\mathbf{c}'_{d-1}}$.

$$\begin{aligned} &\tilde{W}_d(\mathbf{b}'_{d-1}) \stackrel{?}{=} v_{\mathbf{b}'_{d-1}} \wedge \\ &\tilde{W}_d(\mathbf{c}'_{d-1}) \stackrel{?}{=} v_{\mathbf{c}'_{d-1}} \end{aligned}$$

Soundness

Preprocessing:

- Check $\tilde{W}' \stackrel{?}{=} \tilde{W}_0$
- Soundness Error: $s_0/|\mathbb{F}|$

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Sumcheck Round j :

- Check $f_i^{(j)}(r_{j-1}) \stackrel{?}{=} f_i^{(j)}(0) + f_i^{(j)}(1)$
- Soundness Error: $\deg(f_i^{(j)})/|\mathbb{F}| = 2/|\mathbb{F}|$

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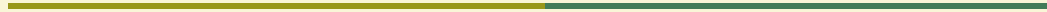
- Check $f_i^{(j)}(r_{j-1}) \stackrel{?}{=} f_i^{(j)}(0) + f_i^{(j)}(1)$
- Soundness Error: $\deg(f_i^{(j)})/|\mathbb{F}| = 2/|\mathbb{F}|$

GKR Round i :

- Check: $m_i \stackrel{?}{=} m'_i$
- Soundness Error: $\frac{1}{|\mathbb{F}|}$

$$\begin{aligned}\delta_s &= \Pr[E_{W'}] + \bigcup_{i=0}^{d-1} \bigcup_{j=0}^{s_i} \Pr[E_j] + \bigcup_{i=1}^{d-1} \Pr[E_i] \\ &\leq \frac{s_0}{|\mathbb{F}|} + \sum_{i=0}^{d-1} \sum_{j=0}^{s_i} \frac{2}{|\mathbb{F}|} + \sum_{i=1}^{d-1} \frac{1}{|\mathbb{F}|} \\ &= \frac{s_0}{|\mathbb{F}|} + \sum_{i=0}^{d-1} s_i \frac{2}{|\mathbb{F}|} + \sum_{i=1}^{d-1} \frac{1}{|\mathbb{F}|} \\ &\leq \frac{\lg(S)}{|\mathbb{F}|} + \sum_{i=0}^d \frac{2 \lg(S)}{|\mathbb{F}|} + \sum_{i=1}^d \frac{1}{|\mathbb{F}|} \\ &= \frac{3d \lg(S) + d}{|\mathbb{F}|}\end{aligned}$$

Efficiency



- Preprocessing: W' , which has size $2^{s_0} = S_0$.
- Round i : $O(s_{i+1})$
 - One sumcheck: $O\left(\sum_{j=1}^{2^{s_{i+1}}} \deg_j\left(f_i^{(j)}\right)\right) = O(s_{i+1})$

Efficiency: Communication $O(S_0 + d \cdot \lg(S))$

- Preprocessing: W' , which has size $2^{s_0} = S_0$.
- Round i : $O(s_{i+1})$
 - One sumcheck: $O\left(\sum_{j=1}^{2^{s_{i+1}}} \deg_j\left(f_i^{(j)}\right)\right) = O(s_{i+1})$

$$O\left(S_0 + \sum_{i=0}^{d-1} s_{i+1}\right) = O\left(S_0 + \sum_{i=0}^{d-1} \lg(S_i)\right) = O(S_0 + d \cdot \lg(S))$$

Efficiency: Verifier $O(S_0 + d \cdot \lg(S) + S + n)$

- Proportional to communication cost: $O(S_0 + d \cdot \lg(S))$
- Evaluating $\widetilde{\text{add}}, \widetilde{\text{mul}}$: $O(t)$ (bounded by $O(S)$)
- The input layer: $O(n)$

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- The input layer: $O(n)$

$$O(S_0 + d \cdot \lg(S) + t + n) = O(S_0 + d \cdot \lg(S) + S + n)$$

Efficiency: Prover Runtime per Sumcheck

$$\begin{aligned} O(2^\ell \cdot T) &= O(2^{2s_{i+1}} \cdot T) \\ &= O(S_{i+1}^2 \cdot (S_i + S_{i+1})) \\ &= O(S_{i+1}^3 + S_i S_{i+1}^2) \end{aligned}$$

Efficiency: Prover Runtime per Sumcheck

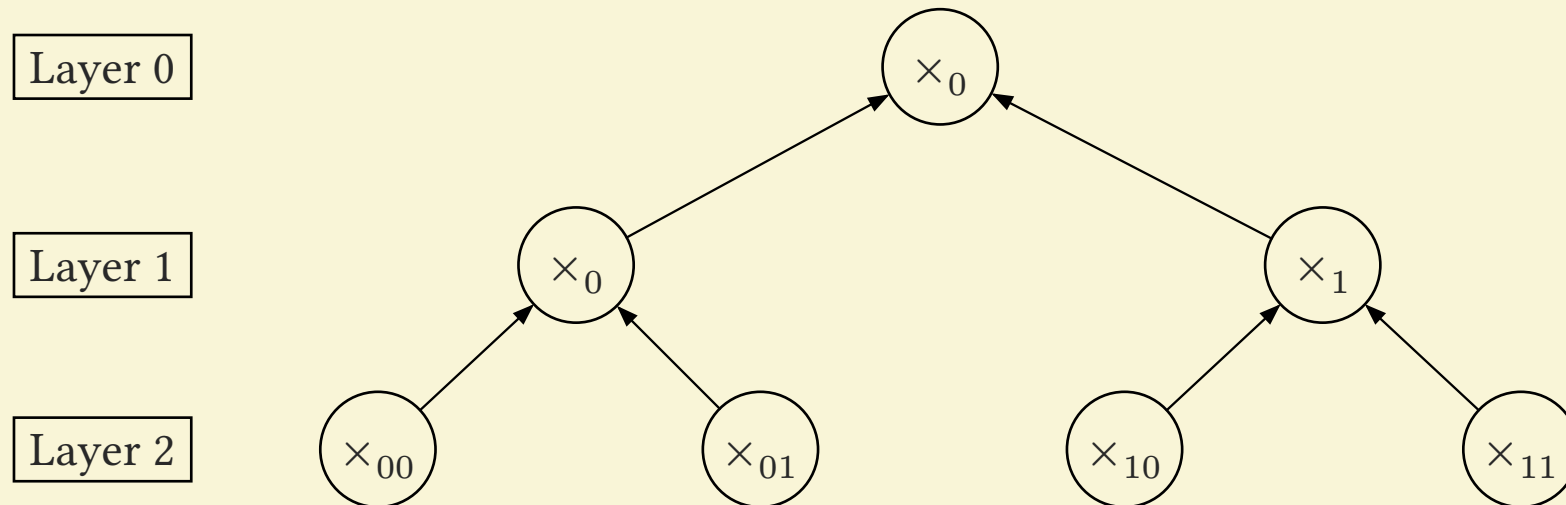
$$\begin{aligned} O(2^\ell \cdot T) &= O(2^{2s_{i+1}} \cdot T) \\ &= O(S_{i+1}^2 \cdot (S_i + S_{i+1})) \\ &= O(S_{i+1}^3 + S_i S_{i+1}^2) \end{aligned}$$

Efficiency: Prover Total

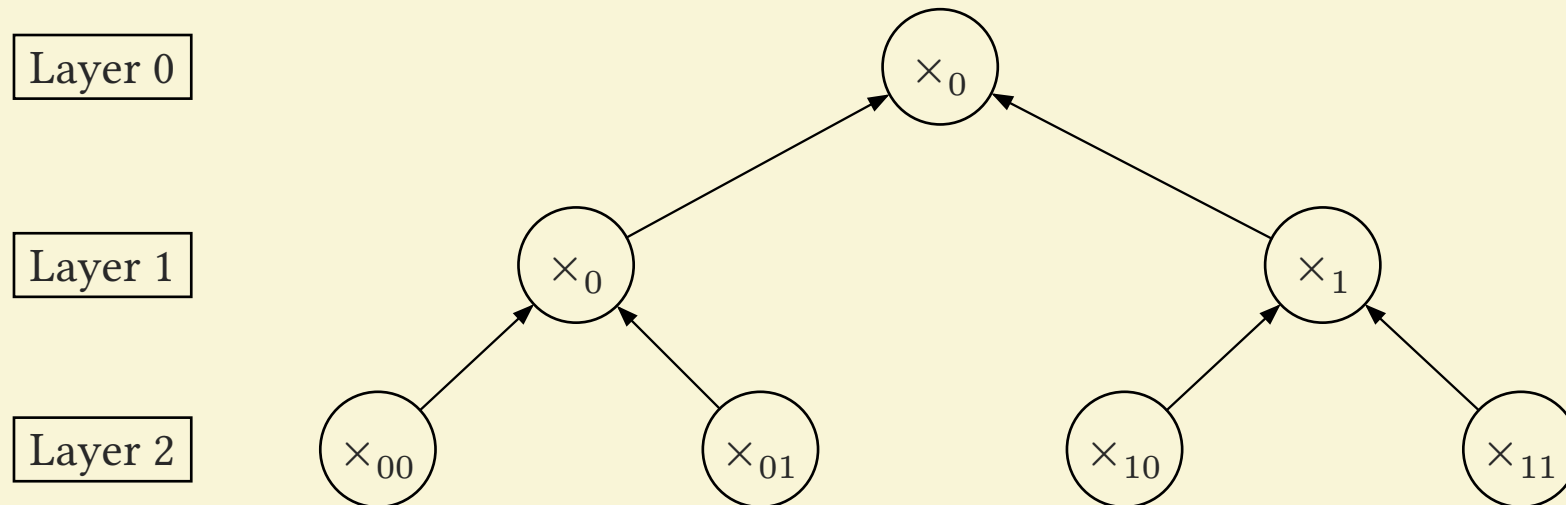
$$O\left(\sum_{i=0}^{d-1} (S_i S_{i+1}^2 + S_{i+1}^3)\right) = O(S^3)$$

Need for Speed - A GKR- Based Grand Product Proof

The Grand Product Proof



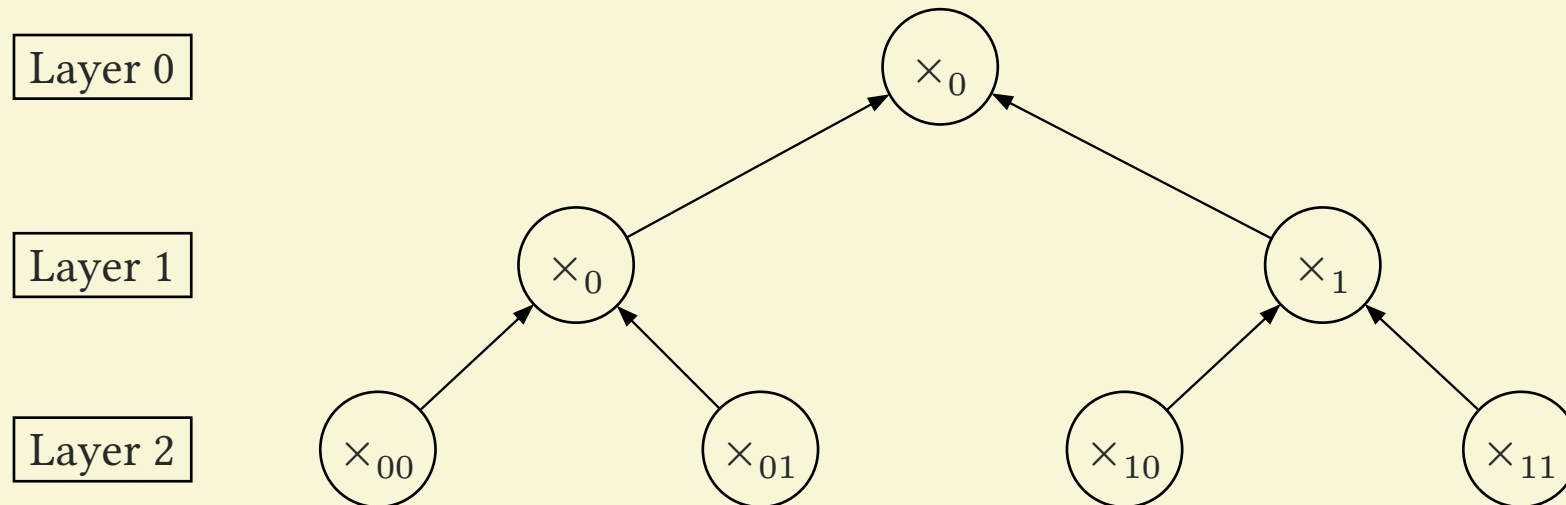
The Grand Product Proof



$$\tilde{W}_i(\mathbf{a}) = \sum_{b \in \mathbb{B}^i} \tilde{e}q(\mathbf{a}, \mathbf{b}) \cdot \tilde{W}_{i+1}(\mathbf{b} \parallel 0) \cdot \tilde{W}_{i+1}(\mathbf{b} \parallel 1)$$

$$s_i = i, \quad S_i = 2^i$$

The Grand Product Proof



$$\tilde{W}_i(\mathbf{a}) = \sum_{b \in \mathbb{B}^i} \tilde{\text{eq}}(\mathbf{a}, \mathbf{b}) \cdot \tilde{W}_{i+1}(\mathbf{b} \parallel 0) \cdot \tilde{W}_{i+1}(\mathbf{b} \parallel 1)$$

$$s_i = i, \quad S_i = 2^i$$

$$S = \sum_{i=0}^d 2^i = 2^{d+1} - 1 = 2n - 1$$

The Sumcheck Polynomial

$$\begin{aligned} q(\mathbf{r}_{i-1}) &= \tilde{W}_{i+1}(\mathbf{r}_{i-1} \parallel 0) + \alpha \tilde{W}_{i+1}(\mathbf{r}_{i-1} \parallel 1) \\ &= \left(\sum_{\mathbf{b} \in \mathbb{B}^i} \tilde{\text{eq}}_{(\mathbf{r}_{i-1} \parallel 0)}(\mathbf{b}) \cdot \tilde{W}_{i+1}(\mathbf{b} \parallel 0) \cdot \tilde{W}_{i+1}(\mathbf{b} \parallel 1) \right) + \\ &\quad \left(\sum_{\mathbf{b} \in \mathbb{B}^i} \alpha \cdot \tilde{\text{eq}}_{(\mathbf{r}_{i-1} \parallel 1)}(\mathbf{b}) \cdot \tilde{W}_{i+1}(\mathbf{b} \parallel 0) \cdot \tilde{W}_{i+1}(\mathbf{b} \parallel 1) \right) \\ &= \sum_{\mathbf{b} \in \mathbb{B}^i} \left(\tilde{\text{eq}}_{(\mathbf{r}_{i-1} \parallel 0)}(\mathbf{b}) + \alpha \cdot \tilde{\text{eq}}_{(\mathbf{r}_{i-1} \parallel 1)}(\mathbf{b}) \right) \cdot \tilde{W}_{i+1}(\mathbf{b} \parallel 0) \cdot \tilde{W}_{i+1}(\mathbf{b} \parallel 1) \end{aligned}$$

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Sumcheck Polynomial:

$$f_i(\mathbf{b}) = \left(\tilde{\text{eq}}_{(\mathbf{r}_{i-1} \parallel 0)}(\mathbf{b}) + \alpha \cdot \tilde{\text{eq}}_{(\mathbf{r}_{i-1} \parallel 1)}(\mathbf{b}) \right) \cdot \tilde{W}_{i+1}(\mathbf{b} \parallel 0) \cdot \tilde{W}_{i+1}(\mathbf{b} \parallel 1)$$

Round Zero?

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$$\begin{aligned}\tilde{W}_0(x) &= \sum_{b \in \mathbb{B}^0} \tilde{eq}(x, b) \cdot \tilde{W}_1(b \parallel 0) \cdot \tilde{W}_1(b \parallel 1) \\ &= \tilde{eq}(x, ()) \cdot \tilde{W}_1(() \parallel 0) \cdot \tilde{W}_1(() \parallel 1) \\ &= \tilde{W}_1(0) \cdot \tilde{W}_1(1)\end{aligned}$$

Full Protocol: Preprocessing

The prover sends evaluations $v_0^{(0)}, v_1^{(0)}$ claiming that $v_0^{(0)} = \tilde{W}_1(0), v_1^{(0)} = \tilde{W}_1(1)$ and thus $y = v_0^{(0)} \cdot v_1^{(0)}$.

$$\begin{array}{l} v_0^{(0)} := \tilde{W}_1(0), \\ v_1^{(0)} := \tilde{W}_1(1) \end{array} \xrightarrow{v_0^{(0)}, v_1^{(0)}} m_0 := v_0^{(0)} \cdot v_1^{(0)}$$

Full Protocol: Round i

$$f_i(\mathbf{b}) = \left(\tilde{\text{eq}}_{(\mathbf{r}_{i-1} \parallel 0)}(\mathbf{b}) + \alpha \cdot \tilde{\text{eq}}_{(\mathbf{r}_{i-1} \parallel 1)}(\mathbf{b}) \right) \cdot \tilde{W}_{i+1}(\mathbf{b} \parallel 0) \cdot \tilde{W}_{i+1}(\mathbf{b} \parallel 1)$$

$$f_i(\mathbf{b}) \xleftarrow{\alpha} \alpha \in_R \mathbb{F}$$

$$\sum_{\mathbf{b}, \mathbf{c} \in \mathbb{B}^i} f^i(\mathbf{b}) \stackrel{?}{=} m_i$$

$$\longleftrightarrow$$

$$\begin{array}{l} v_0^{(i)} := \tilde{W}_{i+1}(\mathbf{r}_i \parallel 0), \\ v_1^{(i)} := \tilde{W}_{i+1}(\mathbf{r}_i \parallel 1) \end{array} \xrightarrow{v_0^{(i)}, v_1^{(i)}} m_i \stackrel{?}{=} \tilde{\text{eq}}_{(\mathbf{r}_{i-1} \parallel 0)}(\mathbf{r}_i) \cdot v_0^{(i)} \cdot v_1^{(i)} + \alpha \cdot \tilde{\text{eq}}_{(\mathbf{r}_{i-1} \parallel 1)}(\mathbf{r}_i) \cdot v_0^{(i)} \cdot v_1^{(i)}$$

At the input layer d , $\mathcal{V}$ has two claims. $\mathcal{V}$ constructs $\tilde{W}_d$ from w and perform a final check.

$$\begin{aligned} v_0^{(d-1)} &\stackrel{?}{=} \tilde{W}_d(r_{d-1} \parallel 0) \wedge \\ v_1^{(d-1)} &\stackrel{?}{=} \tilde{W}_d(r_{d-1} \parallel 1) \end{aligned}$$

The GKR-Based Grand Product Proof - Efficiency

- If each round of sumcheck takes $O(2^{i-j})$ time, then:

$$O\left(\sum_{j=1}^i 2^{i-j}\right) = O\left(\sum_{j=1}^{i-1} 2^{i-j}\right) = O(2^i - 1) = O(2^i)$$

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- Prover cost is dominated by the sumchecks, so this leads to:

$$O\left(\sum_{i=0}^d 2^i\right) = O(2^{d+1}) = O(2^d) = O(n)$$

Linear Prover: Use Lookup Tables

$$\mathbf{a} = (r_1, \dots, r_j, t, x_1, \dots, x_i),$$

$$\begin{aligned} f_i(\mathbf{a}) &= \left(\tilde{\text{eq}}_{(r_{i-1} \parallel 0)}(\mathbf{a}) + \alpha \cdot \tilde{\text{eq}}_{(r_{i-1} \parallel 1)}(\mathbf{a}) \right) \cdot \tilde{W}_{i+1}(\mathbf{a}) \cdot \tilde{W}_{i+1}(\mathbf{a}) \\ &= \left(\hat{\text{eq}}_{(r_{i-1} \parallel 0)}(\mathbf{a}) + \alpha \cdot \hat{\text{eq}}_{(r_{i-1} \parallel 1)}(\mathbf{a}) \right) \cdot \hat{W}_{i+1}(\mathbf{a}) \cdot \hat{W}_{i+1}(\mathbf{a}) \end{aligned}$$

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- Constant time evaluation of f_i !
- $\deg(f_i) + 1 = 4$ points, still constant time.

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- Constant time evaluation of f_i !
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Assuming $\hat{\text{eq}}, \hat{W}_{i+1}$ can be computed in $O(2^{i-j})$ time...

$$\mathbf{v} = (\mathbf{r}_{i-1} \parallel 0) \vee (\mathbf{r}_{i-1} \parallel 1),$$

$$\hat{eq}_j[(b_{j+1}, \dots, b_\ell)] = v_j^{-1} \cdot \hat{eq}_{j-1}[(1, b_{j+1}, \dots, b_\ell)] \cdot v_j r_j + (1 - v_j)(1 - r_j)$$

- $\hat{eq}_0$ can be computed in $O(2^\ell) = O(2^i)$ time
- $\hat{eq}_j$ can be computed in $O(2^{\ell-j}) = O(2^{i-j})$ time.

Computing $\hat{W}_j$:

$$\hat{W}_0[x \in \mathbb{B}^\ell] := W_{i+1}$$

$$\begin{aligned} \hat{W}_j[(x_{j+1}, \dots, x_\ell)] &:= \tilde{e}q_0(r_j) \cdot \hat{W}_{j-1}[(0, x_{j+1}, \dots, x_\ell)] + \\ &\quad \tilde{e}q_1(r_j) \cdot \hat{W}_{j-1}[(1, x_{j+1}, \dots, x_\ell)] \end{aligned}$$

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$$O(2^{\ell-j}) = O(2^{i+1-j}) = O(2^{i-j}) \text{ time!}$$

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$$O(2^{\ell-j}) = O(2^{i+1-j}) = O(2^{i-j}) \text{ time!}$$

Using $\hat{W}_j$:

$$\begin{aligned}\tilde{W}(r_1, \dots, r_{j-1}, t, x_{j+1}, \dots, x_\ell) &= \tilde{e}q_0(t) \cdot \hat{W}_{j-1}[(0, x_{j+1}, \dots, x_\ell)] + \\ &\quad \tilde{e}q_1(t) \cdot \hat{W}_{j-1}[(1, x_{j+1}, \dots, x_\ell)]\end{aligned}$$

Linear Prover!

- Round i : $O(s_{i+1})$
 - One sumcheck: $O\left(\sum_{j=1}^{2^{s_{i+1}}} \deg_j(f_i)\right) = O(i)$

$$O\left(\sum_{i=1}^{d-1} i\right) = O\left(\frac{d(d+1)}{2}\right) = O(d^2) = O(\lg^2(n))$$

$$\begin{aligned} O(\lg^2(n) + t + n) &= O\left(\lg^2(n) + \sum_{i=1}^{d-1} i + n\right) \\ &= O(\lg^2(n) + \lg^2(n) + n) \\ &= O(\lg^2(n) + n) \end{aligned}$$

Conclusion

- *Linear* prover
- Almost *sublinear* verifier
- Sublinear communication costs
- All in the size of the *witness*, not just the circuit

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Excellent construction for SNARKs (Spartan, Lasso)

Fin